

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Othmar Leitner, et al.                      Art Unit : 2822  
Serial No. : 10/579,487                                      Examiner : Kevin Picardat  
Filed : May 16, 2006                                        Confirmation No.: 3015  
Notice of Allowance Date: March 23, 2009  
Title : METHOD FOR THE PRODUCTION OF TRANSISTOR STRUCTURES WITH  
LDD

**MAIL STOP ISSUE FEE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed March 23, 2009, submitted herewith are a completed issue fee transmittal form PTOL-85b and payment in the amount of \$1,810 for the required issue and publication fees.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

June 23, 2009  
Date: \_\_\_\_\_

/Paul Pysher/  
\_\_\_\_\_  
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Reg. No. 40,780

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